


# Kathir College of Engineering

## Faculty Profile

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<div>M. Thamaraiselvi</div> <div>Assistant Professor</div> <div>ECE</div>		
Qualification		M.E
Area of Specialization		VLSI Design
Email		thamaraiselvi@kathir.ac.in
Mobile Number		9629163577
Membership in Professional Bodies		IAENG - 311479
Total Experience in (Years) : 7 Years 10 Months		
Industry		Academic
Experience in Years : 3		Experience in Years : 4 years 10 Months
Publication Details		
Number of Papers Published	Journal Publications	Conference Publications
	2	11
<div>1. Thamaraiselvi, M. and Babu, G.M., 2012. Design of High Accuracy Fixed Width Modified Booth Multiplier for MAC Unit. Digital Signal Processing, pp.142-145."</div> <div>2. Published a paper titled "Log Co-ordinate Mapping Based Audio Watermarking" in International Journal of Engineering Research and Technology in 2014 and IJERT Conference Proceeding Volume 3, Issue 08</div>		

## Conferences

1. Presented a paper titled "Recent advances in machine learning based flexible electronics application" in the **AICTE Sponsored National Conference** on Recent Trends in Artificial Intelligence , Advanced Communication and Smart Systems" March 2023
2. Presented a paper titled "AI Chatbot using Neural Network" in 2<sup>nd</sup> International Conference on Intellectual Research in Science, Engineering and Management (ICIRSEM 2022)
3. International Conference on Computer, Communication and Signal Processing held at SSM College of Engineering, paper titled "Design of MAC Unit using High Accuracy Fixed – Width Modified Booth Multiplier"
4. International Conference on Recent Innovations in Engineering held at Sri Subramanya College of Engineering and Technology, Palani, Paper titled "Support Vector Machine Algorithm Based Classification of Cancer Images"
5. National Conference on Electrical Systems held at RVS College of Engineering and Technology, paper titled "Design of Fixed – Width Modified Booth Multiplier for High Accuracy Applications"
6. National Conference on VLSI, Communication and Computation held at Maharaja Engineering College, paper titled "Design of High Accuracy Fixed – Width Modified Booth Multiplier for MAC Unit"
7. National Conference on Signal Processing, Communication and VLSI Design held at Anna University Regional Centre, Coimbatore, paper titled "Log Co-Ordinate Mapping Based Audio Watermarking"
8. FUNTELECTA 2013 Two Days National Conference held at Palakkad Institute of Science and Technology, paper titled "Data Embedding Using Adaptive Pixel Pair Matching"
9. National Conference on VLSI, Communication and Computation held at Maharaja Engineering College, paper titled "Modeling and Control of a Snake Like Robot Using the Screw Drive Mechanism"
10. National Conference on VLSI, Communication and Computation held at Maharaja Engineering College, paper titled "An Efficient Data Embedding Method Through Optimization of Pixel Pair Matching Technique"
11. National Conference on Emerging Trends in Engineering and Technology held at

Younus College of Engineering and Technology, paper titled "Log co-ordinate Mapping Based Audio Watermarking"	
<b>Research Interests</b>	<b>Type your Research Interests here:</b>
<b>Other relevant achievements, awards and links:</b>	<b>Type any other relevant links/Information here:</b> <ol style="list-style-type: none"> <li>1. Got General Proficiency Award in PG</li> <li>2. Got Gold Medal in Post Graduation</li> <li>3. Faculty Development Programme on Tanner EDA and Mentor Graphics Tools at Tamil Nadu College of Engineering, Coimbatore</li> <li>4. Attended DHR-ICMR Sponsored Two days National Level Workshop on Clinical Training/Translational Research on Integrated Care Model for Mental and Neurological Disorder at KPRIET</li> <li>5. Attended 6 days Faculty Development Programme on VLSI and Chip Design conducted in physical mode at PSGiTech</li> <li>6. Attended 6 days ATAL FDP on Recent Trends in Semiconductor System Design and Testing Techniques</li> </ol>

**Note: Please fill in and submit in the drive link in respective department folder**

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